Biographical Sketch

Remigiusz Wisniewski is Professor and Head of the *Division of Information Systems and Cybersecurity* (Institute of *Control & Computation Engineering*) at the University of Zielona Góra, Poland. He received his M.Sc. and Ph.D. degrees in computer science from University of Zielona Góra in 2003 and 2008, respectively. In 2018 he obtained a D.Sc. degree (habilitation) in computer science from the Silesian University of Technology, Gliwice, Poland. From 1999 to 2002 he was with *Aldec Inc.* (Henderson, NV, USA), where he conducted specialized trainings for companies, such as *Xilinx* (San Jose, CA, USA), *Intel* (Austin, TX, USA). In 2001 he conducted presentations for *Aldec Inc.*



at DAC and DATE conferences (Las Vegas, NV, USA and Munich, Germany, respectively). In 2019 he was a visiting professor at the *University of California*, *Berkeley*, CA, USA, where he took up an internship.

Prof. Wisniewski has published more than 100 publications, including 2 books, 40+ journal papers, and 6 book chapters. His main research interests include cryptography, modeling, design, decomposition and analysis of concurrent control systems, cyber-physical systems, Petri nets, programmable devices, field programmable gate arrays (FPGAs).

Prof. Wisniewski is a Senior Member of *IEEE*, and a member of *IES* and *SMC* societies, as well as *IEEE IES Technical Committee on Industrial Informatics*. He is a member of *Advisory Board at Polish Ministry of Science and Higher Education* ("Industrial Doctorate" program).

Prof. Wisniewski currently serves as an Associate Editor for the journal *IEEE Transactions on Systems, Man, and Cybernetics: Systems*, and for the journal *IEEE Access*, where in July 2019 he was chosen as an *Associate Editor of the Month*. He lead or served as a guest editor for several special sections organized within *IEEE Transactions on Systems, Man, and Cybernetics: Systems, Mathematics, International Journal of Applied Mathematics and Computer Science, <i>IEEE Access, Applied Sciences, Computer Communications*.

He was a chairman or co-organizer of special sessions and technical tracks organized during conferences: *IEEE Annual Conference of the Industrial Electronics Society* (IEEE IES IECON), *International Conference of Computational Methods in Sciences and Engineering* (ICCMSE), *IEEE International Conference on Human System Interaction* (IEEE HSI), *IEEE International Conference on Industrial Informatics*, (IEEE INDIN).

He is a Co-Founder and a Coordinator of the research projects *Hippo* and *Hippo-CPS*, related to the prototyping, analysis and decomposition of control systems and control part of cyber-physical systems specified by Petri nets (http://www.hippo.iee.uz.zgora.pl).

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